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JPW

In re Patent Application of: Peter J. Hopper et al.

Atty Docket No. NSC1-M3000 [P05657]

Application No.: 10/647,604

Filed: August 25, 2003

Confirmation No.: 2816

For: ULTRA LOW LEAKAGE MOSFET TRANSISTOR

M/S AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

Transmittal herewith is an amendment in the above-identified application.

The fee has been calculated as shown below.

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDITIONAL FEE
TOTAL	8	MINUS	20		x \$50 =	\$0.00
INDEP.	2	MINUS	3		x \$200 =	\$0.00
FIRST PRESENTATION OF MULTIPLE DEP CLAIMS					+ \$360	\$0.00
					TOTAL	\$0.00

Small Entity 50% Filing Fee Reduction (if applicable)

\$

- \* If the entry in Col. 1 is less than the entry in Col. 2, write "0" in Col. 3  
\*\* If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, write "20" in this space.  
\*\*\* If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, write "3" in this space. The "Highest Number Previously Paid For" (Total or Independent is the highest number found from the equivalent box in Col. 1 of a prior amendment or the number of claims originally filed.)

- ☒ No additional fee is required.
- ☐ A check in the amount of \$\_\_\_\_\_ is attached.
- ☒ Please charge any additional fees, including any fees necessary for extensions of time or credit overpayment to Deposit Account No. 50-1703, under Order No. NSC1-M3000.  
**A duplicate copy of this sheet is enclosed.**
- ☐ Petition for extension of time. The undersigned attorney of record hereby petitions for an extension of time pursuant to 37 C.F.R. § 1.136(a), as may be required, to file this response.

STALLMAN & POLLOCK LLP

Dated: June 23, 2005

By:

Michael J. Pollock (Reg. No. 29,098)  
Attorneys for Applicant(s)

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on June 23, 2005.

Dated: 06/23/05

By:

  
Lana T. Brenner

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Peter J. Hopper et al.

Application No.: 10/647,604

Filed: August 25, 2003

For: ULTRA LOW LEAKAGE MOSFET  
TRANSISTOR

Group No.: 2826

Examiner: Mondt, Johannes P.

**SUPPLEMENTAL RESPONSE  
TO OFFICE ACTION MAILED  
JANUARY 13, 2005**353 Sacramento Street, Suite 2200  
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Box 1450, Alexandria, VA 22313-1450 on June 23, 2005.  
STALLMAN & POLLOCK LLPDated: 06/23/05By: 

Lana T. Brenner

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Please amend the above-identified application as follows:

Amendments to the Specification are provided on page 2 of this paper.

Amendments to the claims are provided in the Listing of the Claims that begins on page 3  
of this paper.

Remarks begin on page 5 of this paper.